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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))				Docket No. QN1072.US	
In Re Application Of: Steven M. Betker					
Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/798,468	03/11/04	Not Assigned	22145	2661	6100
Title: Method and System for Reducing Deadlock in Fibre Channel Fabrics using Virtual Lanes					
<div style="display: flex; justify-content: space-between; align-items: flex-start;"> <div style="width: 20%;">  </div> <div style="width: 80%; text-align: center;"> <p>Address to:</p> <p>Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</p> <p>37 CFR 1.97(b)</p> <p>1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</p> <p>37 CFR 1.97(c)</p> <p>2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</p> <div style="margin-left: 40px;"> <input type="checkbox"/> the statement specified in 37 CFR 1.97(e); </div> <p style="text-align: center; margin: 10px 0;">OR</p> <div style="margin-left: 40px;"> <input type="checkbox"/> the fee set forth in 37 CFR 1.17(p). </div> </div> </div>					

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Docket No.
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10/798,468	03/11/04	Not Assigned	22145	2661	6100

Title: Method and System for Reducing Deadlock in Fibre Channel Fabrics using Virtual Lanes



Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 11-1159 as described below.
- ☐ Charge the amount of _____
- ☒ Credit any overpayment.
- ☒ Charge any additional fee required.
- ☐ Payment by credit card. Form PTO-2038 is attached.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

Certificate of Transmission by Facsimile*

I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. _____)

(Date)

Signature

Typed or Printed Name of Person Signing Certificate

Certificate of Mailing by First Class Mail

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450", [37 CFR 1.8(a)] on _____

10/13/04
(Date)

Signature of Person Mailing Correspondence

Richa Dhindsa

Typed or Printed Name of Person Mailing Certificate

*This certificate may only be used if paying by deposit account.

James G. O'Neill
Signature

Dated: 10/13/2004

James G. O'Neill
Registration No: 22858
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Tel: 949 955 1920
Fax: 949 955 1921

CC:



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven M. Betker.
Title: Method and System for Reducing Deadlock in Fibre Channel Fabrics Using Virtual Lanes
Serial No. 10/798,468
Filed: 03/11/04
Docket No. QN1072.US

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 and 1.98, enclosed materials are brought to the attention of the Examiner for consideration in connection with the above identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached form 1449 be considered by the Examiner and made of record.

Pursuant to 37 C.F.R. § 1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 11-1159 in order to have this Information Disclosure Statement Considered.

If the Examiner has any questions concerning this IDS, the examiner is requested to contact the undersigned. If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule and charge the appropriate fee to Deposit Account No. 11-1159.


If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 11-1159 for any additional fees required under 37 C.F.R. § 1.16 or under § 1.17; particularly, extension of time fees.

Respectfully submitted,

Klein, O'Neill & Singh, LLP

10/13/2004
Dated:

By James G. O'Neill
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Irvine, CA 92614
Tel: 949 955 1920
Fax: 949 955 1921

	ATTY DOCKET NO.	APPLICATION NO.
	ON1072.US	10/798,468
	APPLICANT(S)	
	Steven M. Betker	
FILING DATE		GROUP ART UNIT
03/11/04		2661

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,697,359	02/24/2004	George	370	357	
	6,597,691	07/22/2003	Anderson et al	370	360	
	6,449,274	09/10/2002	Holden et al	370	392	
	6,424,658	07/23/2002	Mathur H.	370	429	
	6,411,599	06/25/2002	Blanc, Alain, et l	370	219	

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	US-2001/0038628	11/08/2001	Ofek, et al.	370	392	
	US-2003/0016683	01/23/2003	George et al	H04L	12/28	
	US-2003/0179748	09/25/2003	George et al	370	389	
	US-2004/0141521	07/22/2004	George	370	463	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	EP0649098	09/19/1994	EP	G06F	15/16	✓	
	EP-0856969	01/30/1998	EP	H04L	12/00	✓	
	WO-98/36537	08/20/1998	PCT	H04L	12/44	✓	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	CLARK, TOM, "Zoning for Fibre Channel Fabrics", Vixel Corporation Paper - XP002185194., (Aug. 1999), pp 1-6
	Malavalli, Kumar, et al., "Distributed Computing with fibre channel fabric", Proc of the Computer Soc. Int'l Conf., Los Alamitos, IEEE Comp Soc. Press., Vol. Conf. 37, XP000340745, (Feb. 24, 1992), pp 269-274

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	ATTY DOCKET NO. QN1072.US		APPLICATION NO. 10/7998,468	
	Steven M. Betker			
	FILING 03/11/04		GROUP ART 2661	

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,330,236	12/11/2001	Ofek, et al	370	369	
		6,324,181	11/27/2001	Wong et al	370	403	
		6,308,220	10/23/2001	Mathur H.	709	238	
		6,160,813	12/12/2000	Banks et al	370	422	
		6,128,292	10/03/2000	Kim et al	370	356	

U.S. PATENT APPLICATION PUBLICATIONS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		US2004/0028038	02/12/2004	Anderson et al	370		
		US2004/0013125	01/22/2004	Betker et al			
		US2004/0013092	01/22/2004	Betker et al			

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>			
			Martin, Charles R., "Fabric Interconnection of fibre channel standard nodes", Proceedings of the SPIE, (September 8, 1992), pp 65-71
			Yoshida, Hu, "LUN Security Considerations for Storage Area Networks," Hitachi Data Systems Paper - XP 002185193 (1999), pp 1-7

EXAMINER	DATE CONSIDERED
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INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	ATTY DOCKET NO. QN1072.US	APPLICATION NO. 10/798,468
	Steven M. Betker	
	FILING 03/11/04	GROUP ART 2661

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,081,512	06/27/2000	Muller et al	370	256	
	6,047,323	04/01/2000	Krause, Michael R.	709	227	
	6,021,128	02/01/2000	Hosoya et al	370	380	
	6,014,383	01/11/2000	McCarty, James F.	370	453	
	5,999,528	12/07/1999	Chow Henery et al	370	365	

U.S. PATENT APPLICATION PUBLICATIONS

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

		Claudio DeSanti, "Virtual Fabrics Switch Support" ; VF Switch Support, T11/04-395v2. September 2004, pp 1-15.
		Pelissier et al, "Inter-Fabric Routing" , dated July 30, 2004, Inter Fabric Routing (04-520v0); pp 1-31

EXAMINER	DATE CONSIDERED
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INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	ATTY DOCKET NO. QN1072.US	APPLICATION NO. 10/798,468
	Steven M. Betker	
	FILING 03/11/04	GROUP ART 2661

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,987,028	11/16/1999	Yang et al.	370	380	
	5,818,842	10/06/1998	Burwell et al	370	397	
	5,748,612	05/05/1998	Stoevhase et al.	370	230	
	5,687,172	11/11/1997	Cloonan et al	370	395	
	5,610,745	03/11/1997	Bennett, Dwayne	359	139	

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

			DeSanti et al, "Virtual Fabrics"; Virtual Fabrics, T11/03-352v0, May 2003; pp 1-4
			Martin et al , "Virtual Channel Architecture" , Presentation by Brocade to T11/03-369V0 dated June 2, 2003

EXAMINER	DATE CONSIDERED
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	Steven M. Betker	
	FILING 03/11/04	GROUP ART 2661

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,598,541	01/28/1997	Malladi, S. R.	395	286	
	5,367,520	11/22/1994	Cordell, Robert R.	370	60	
	5,144,622	09/01/1992	Takiyasu, T. et al	370	85.13	
	4,821,034	04/11/1989	Anderson et al	340	825	
	4,725,835	02/16/1988	Schreiner et al	340	825.83	

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER

DATE CONSIDERED

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INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	Docket Number (Optional)	Application Number
	QN1072.US	10/798,468
	Applicant(s)	
	Steven M. Betker	
	Filing Date	Group Art Unit
	03/11/04	2661

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		4,569,043	02/04/1986	Simmons et al	370	63	
		4,546,468	10/08/1985	Christmas et al	370	54	
		4,425,640	01/10/1984	Philip et al	370	58	
		4,162,375	07/24/1979	Schilichte	179	15	

U.S. PATENT APPLICATION PUBLICATIONS

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FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
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OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER	DATE CONSIDERED
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven M. Betker.
Title: Method and System for Reducing Deadlock in Fibre Channel Fabrics using Virtual Lanes
Serial No. 10/798,468
Docket No. QN1072.US

Filed: 03/11/04

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sirs,

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified application:

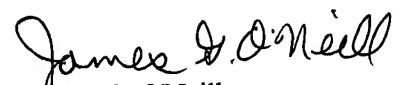
<u>Serial/Patent No.</u>	<u>Filing date</u>	<u>Attorney Docket</u>	<u>Title</u>
10/266,360	10/07/2002	QN1010.US	Method And System For Reducing Congestion In Computer Networks
10/241,153	09/11/2002	QN1016.US	Zone Management In A Multi-Module Fibre Channel Switch
10/263,858	10/2003/2002	QN1017.US	Method And System For Using Distributed Name Servers In Multi-Module Fibre Channel Switching
10/212,425	08/05/2002	QN1020.US	Method And System For Flexible Routing In A Fibre Channel System
10/302,149	11/22/2002	QN1027.US	Method And System For Controlling Packet Flow In Networks
10/894579	07/20/2004	QN1044.US	Method and System for Managing Traffic in Fibre Channel Switches
10/894546	07/20/2004	QN1050.US	Method and System for Routing and Filtering Network Data Packets in Fibre Channel Systems
10/894,827	07/20/2004	QN1058.US	Method and System for Selecting Virtual Lanes in Fibre Channel Switches
10/894597	07/20/2004	QN1059.US	Programmable Pseudo Virtual Lanes for Fibre Channel Systems
10/894595	07/20/2004	QN1061.US	Method and System for Reducing Latency and Congestion in Fibre Channel Switches

10/664,548	09/19/2003	QN1062.US	Buffer to Buffer Credit Recovery for In-Line Fibre Channel Credit Extension Devices
10/895175	07/20/2004	QN1063.US	Method and System for Detecting Congestion and Over Subscription in a Fibre Channel Network
10/894492	07/20/2004	QN1065.US	LUN Based Hard Zoning in Fibre Channel Switches
10/894587	07/20/2004	QN1066A.US	Multi Speed Cut Through Operation in Fibre Channel Switches
10/894726	07/20/2004	QN1066B.US	Method and System for Improving Bandwidth & Reducing Idles in Fibre Channel Switches
10/894629	07/20/2004	QN1066C.US	Method and System for Routing Fibre Channel Systems
10/894491	07/20/2004	QN1066D.US	Method and System for Keeping a Fibre Channel Arbitrated Loop Open During Frame Gaps
10/894536	07/20/2004	QN1066E.US	Method And System For Congestion Control Based On Optimum Bandwidth Allocation In A Fibre Channel Switch
10/894627	07/20/2004	QN1068.US	Method and System for Programmable Data Dependent Network Routing
10/719,077	11/21/2003	QN1069.US	Method and System for Monitoring Events in Storage Area Networks
10/894,689	07/20/2004	QN1070.US	Method and System for Power Control of Fibre Channel Switches
10/798,527	3/11/2004	QN1071.US	Method and System for Preventing Deadlock in Fibre Channel Fabrics using Frame Priorities
10/894586	07/20/2004	QN1073.US	Method and System for Buffer to Buffer Credit recovery in Fibre Channel Systems Using Virtual and/or Pseudo Virtual Lane
10/889,635	07/12/2004	QE1075.US	Method And System For Inexpensive And Non-Disruptive Data Capture In Networks
10/889,337	07/12/2004	QE1076.US	Method And System For Minimizing Disruption In Common-Access Networks
10/889259	07/12/2004	QE1078.US	Method And Apparatus For Detecting And Removing Orphaned Primitives In A Fibre Channel Network
10/889267	07/12/2004	QE1079.US	Method And System For Fibre Channel Arbitrated Loop Acceleration
10/889551	07/12/2004	QE1080.US	Method And Apparatus For Testing Loop Pathway Integrity In A Fibre Channel Arbitrated Loop
10/889256	07/12/2004	QE1081.US	Method And Apparatus For Accelerating

10/889255	07/12/2004	QE1082.US	Receive-Modify-Send Frames In A Fibre Channel Network
10/889,588	07/12/2004	QE1083.US	Method and Apparatus for Test Pattern Generation
10/894597	07/20/2004	QN1086.US	Method And Apparatus For Improving Buffer Utilization In Communication Networks
10/894978	07/20/2004	QN1092.US	Method And System For Using Extended Fabric Features With Fibre Channel Switch Elements
10/894917	07/20/2004	QN1096.US	Method And System for Programmable Data Dependent
10/894529	07/20/2004	QN1100.US	Method and System for Configuring Fibre Channel Ports
10/894732	07/20/2004	QN1101.US	Integrated Fibre Channel Fabric Controller
	10/01/2004	QN1102.US	Method And System For Congestion Control In A Fibre Channel Switch
	10/01/2004	QN1116.US	Method and System for Using Boot Servers in Fibre Channel Network Routing
	10/01/2004	QN1129.US	Method and System for Transferring Data directly between storage devices in a Storage Area Networks
	10/01/2004	QN1131.US	High Speed Fibre Channel Switch Elements
	10/08/2004	QN1132.US	Method and System for LUN Remapping in Fibre Channel Networks
	10/01/2004	QN1136.US	Fibre Channel Transparent Switch For Mixed Switch Fabrics
			Method and System for Using an In-Line Credit Extender with a Host Bus Adapter

Respectfully Submitted,

Dated: 10/13/2004


 James G. O'Neill
 Attorney for the Applicant
 Klein, O'Neill & Singh, LLP
 2 Park Plaza, Suite 510
 Irvine, CA 92614
 Tel: 949 955 1920
 Fax: 949 955 1921

Certificate under 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as priority mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on this 13th day of October, 2004.

Name: Richa Dhillon Signature: Richa Dhillon